

Abstract

Storage Device for a Multibus Architecture

Storage device for a multibus architecture comprising

- at least one memory (M) to store data (d), information and/or addresses,
- a memory connection (B) including a port (B0) to connect the memory (M) to a first bus (D0) of a multibus architecture (P, D0, D1),
- wherein the memory connection (B), the port (B0), and the first bus (P) have data lines (DL) to transmit the data (d), and, as required, transmit addresses and/or control information in order to control the memory (M), and, as required, other devices connected to the relevant bus (D0).

In order to achieve more effective memory management, it is proposed that a switching device (SW) be provided to selectively connect the memory connection (B) to the first bus (D0) and to one or more additional buses (D1, P) to effect a memory access in order to transmit data, addresses, and/or control information from or to these buses.

Figure 1